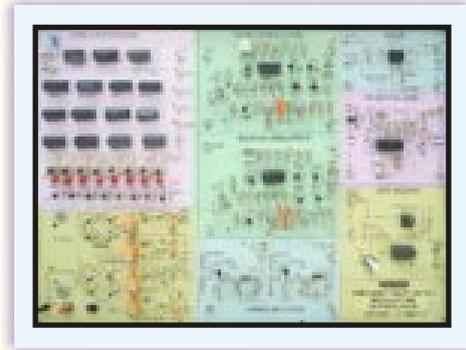




FSK MODULATION/DEMODULATION TRAINER WITH BIT ERROR MEASUREMENT

MODEL - COM115BIT

This trainer has been designed with a view to provide practical/experimental knowledge of FSK Modulation /Demodulation with Bit Error Measurement on a SINGLE PCB.



SPECIFICATIONS

1. Power supply requirement : 230V AC, 50 Hz.
2. Built in IC based power supply.
3. On Board Modulating Digital Data signal generator to generate any binary input word with Bit clock & Word clock.
Word Length : 8 Bits.
Data Format : NRZ.
4. On Board RF carrier signal generator.
Frequency Range : 320KHz and 640 KHz.
Amplitude : 2 Vpp.
5. Modulator Type : Balanced modulator.
6. Built-in Gaussian Noise Generation and Matched Filter
7. Demodulator Type : FSK Demodulator and Data square.
8. All parts are soldered on single pin TAGS on single PCB with complete circuit diagram Screen-printed.
9. Standard Accessories :
 1. A Training Manual
 2. Connecting Patch cords.

EXPERIMENTS

1. To Observe FSK Modulation and Demodulation Waveforms on CRO.
2. To Observe Spectrum of FSK Modulation Signal and determine Essential bandwidth.
3. To Measure Bit error rate in presence of channel noise
4. To analyze reception quality by cross co-relation characteristics using Matlab Simulink software.

In keeping view of SIGMA policy of continuous development and improvement, the Specifications may be changed without prior notice or obligation.

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